

elpauer.org

Wednesday 18th of August 2004

Multiplexing CAS/RAS signals

Filed under:

- [Ideas](#)

– pgquiles @ 14:34:01 [Edit This](#)

Warning Very specific talking: RAM memories (low level)

RAM memories send the column number, then wait a few cycles, then send the CAS signal, then wait another few cycles, then send the row number, then wait more cycles, then send the RAS signal. You have to wait a lot of cycles for electric signals to stabilize.

I wonder why they don ' t take advantage of base band code–division multiple access for multiplexing row & column numbers and CAS & RAS signals. I guess it ' s too much money for a little optimization, but I was very disappointed when nobody at the DISCA department of the Technical University of Valencia could answer my question.

Comments »

The URI to TrackBack this entry is: <http://www.elpauer.org/wordpress/wp-trackback.php/116>

No comments yet.

RSS feed for comments on this post.

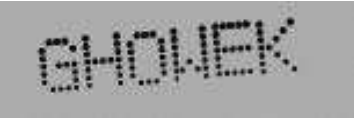
Leave a comment

Line and paragraph breaks automatic, e–mail address never displayed, HTML allowed: <abbr title=""> <acronym title=""> <blockquote cite=""> <code> <i> <strike>

Name

E–mail

URI

Enter this code: 

Your Comment



Say It!

pgquiles@elpauer.org

Powered by **WordPress**